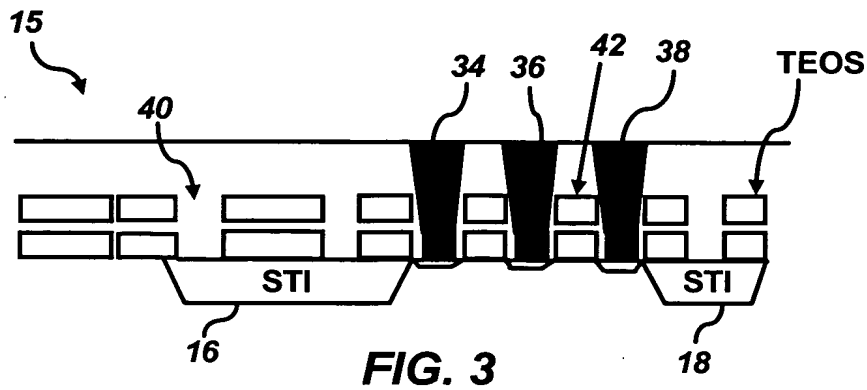
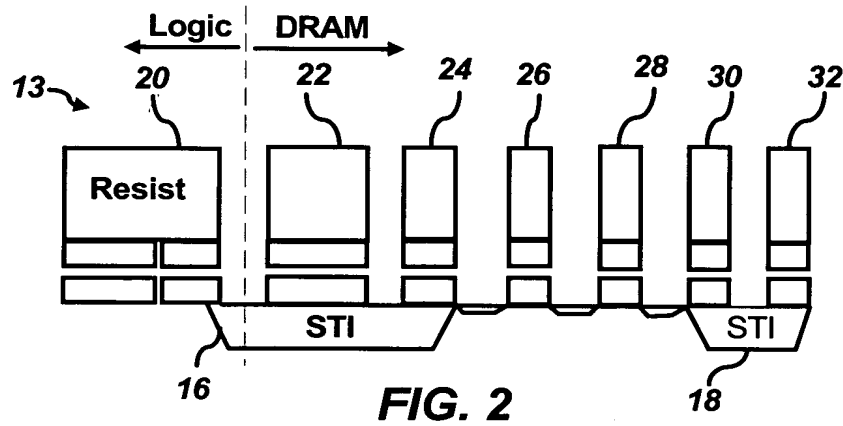
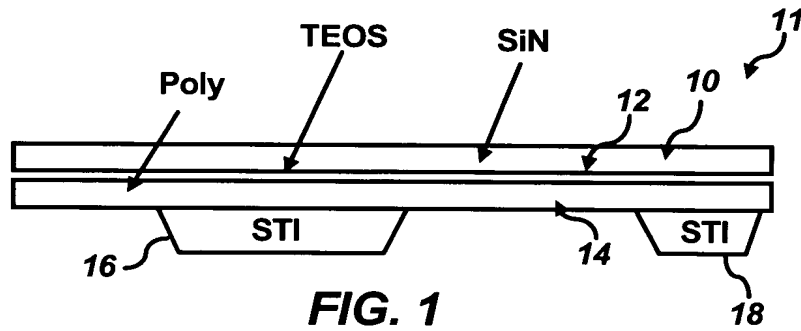
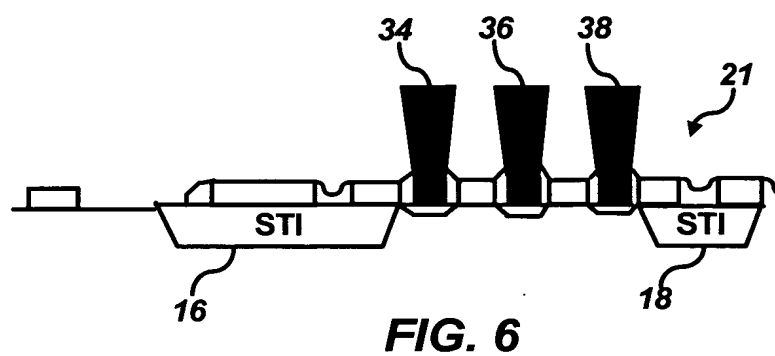
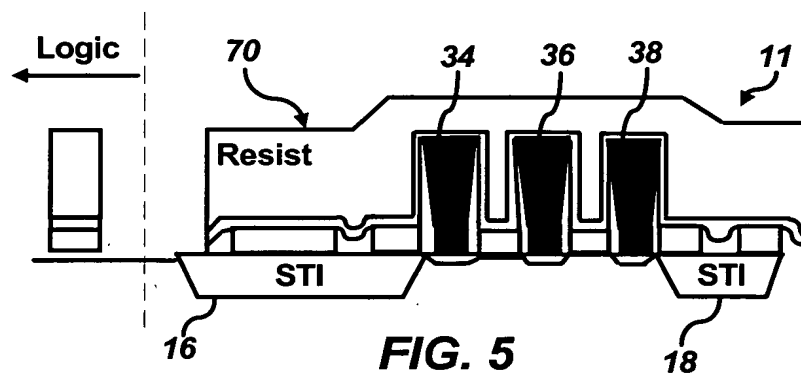
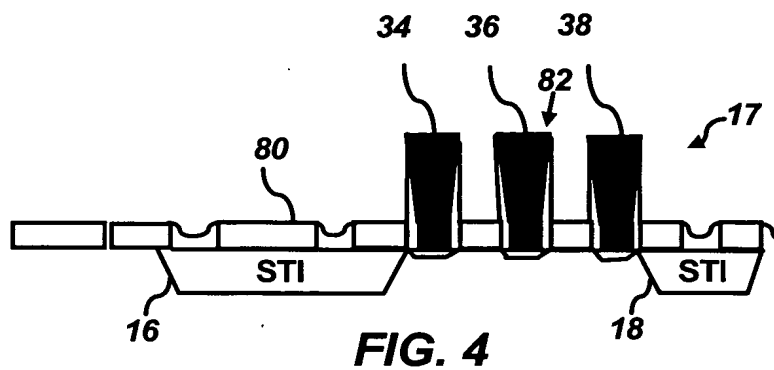


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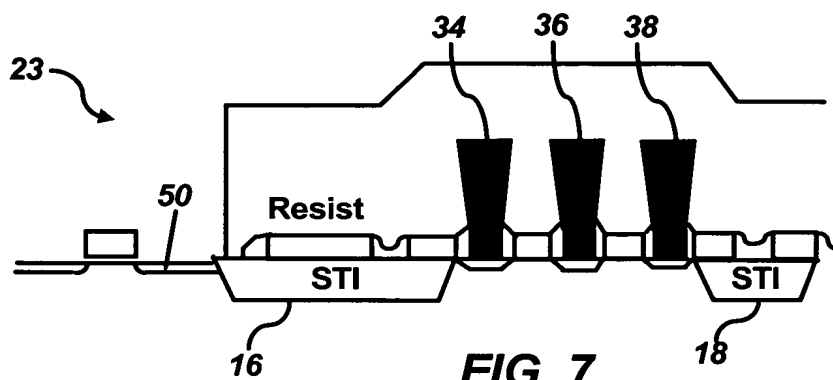


FIG. 7

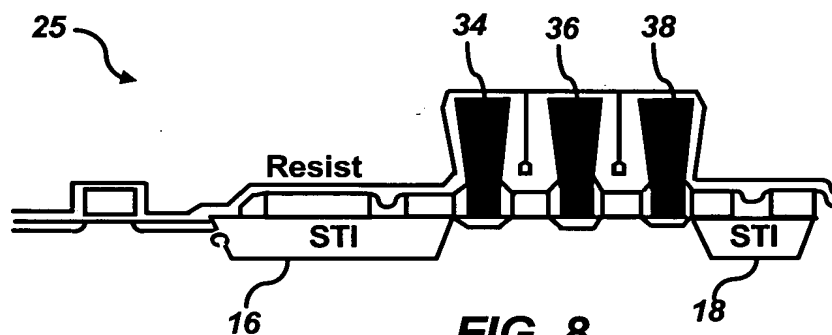


FIG. 8

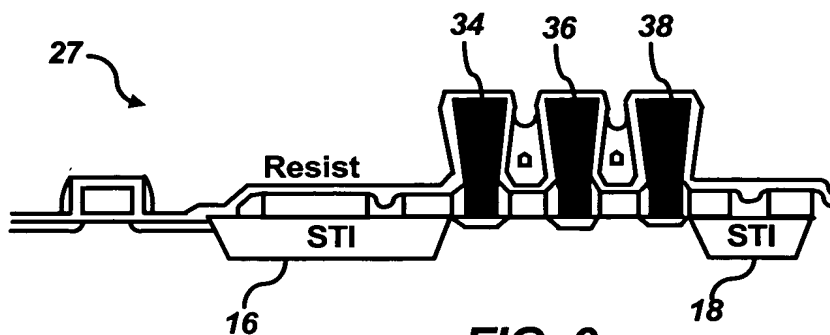


FIG. 9

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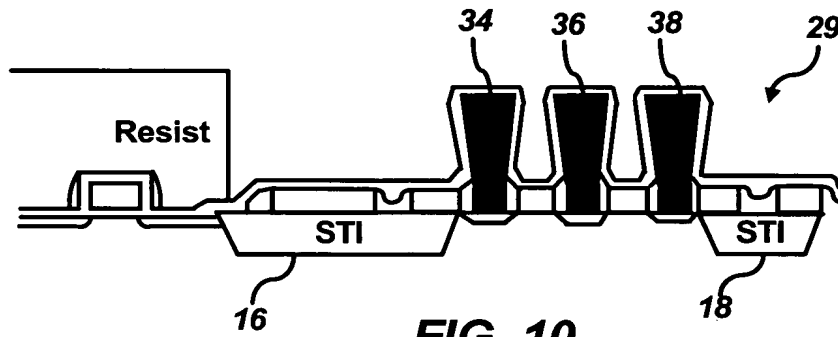


FIG. 10

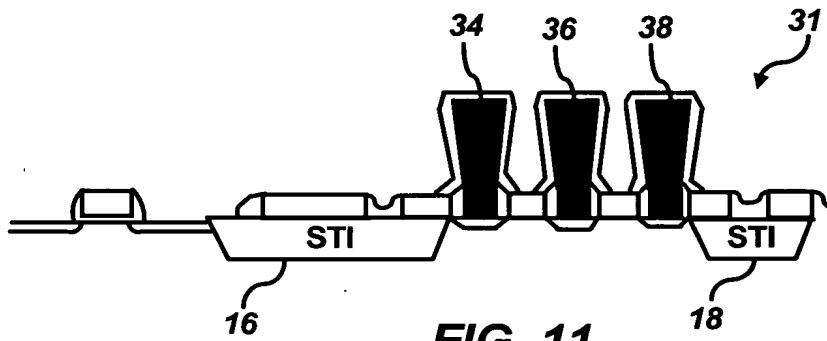


FIG. 11

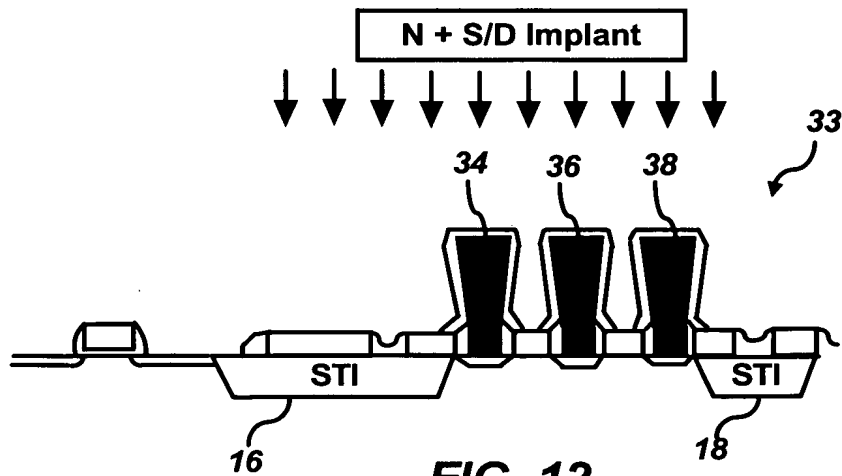


FIG. 12



Inventor(s): Min-Hsiung Chiang

Serial No.: To Be Assigned

Filed: Herewith

For: Compatible Embedded DRAM Process For MIM Capacitor

Attorney Docket No.: 67,200-547

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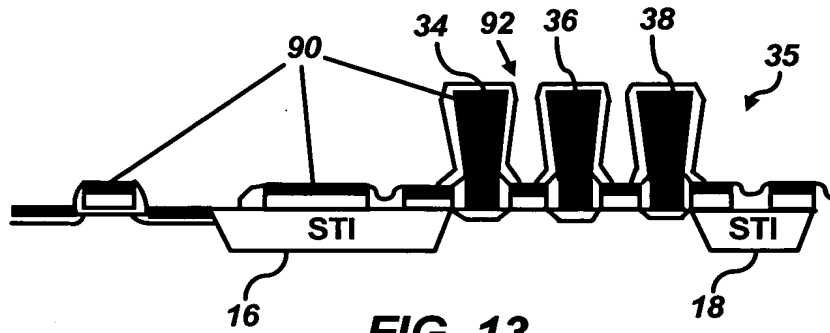


FIG. 13

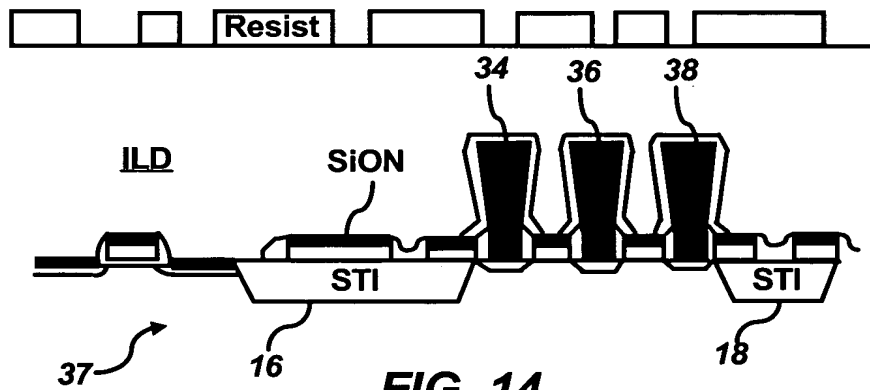


FIG. 14

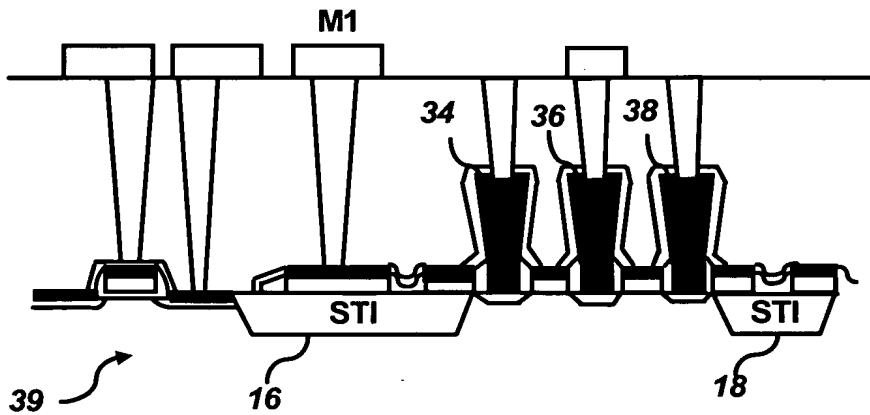


FIG. 15

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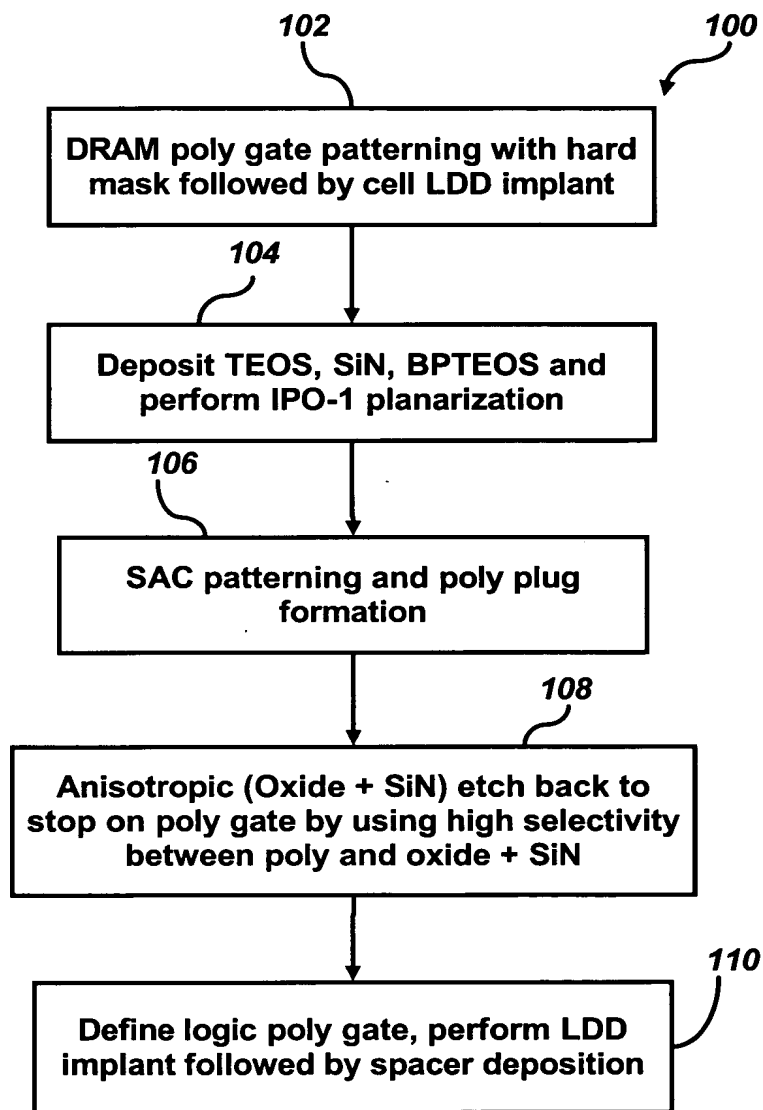


FIG. 16

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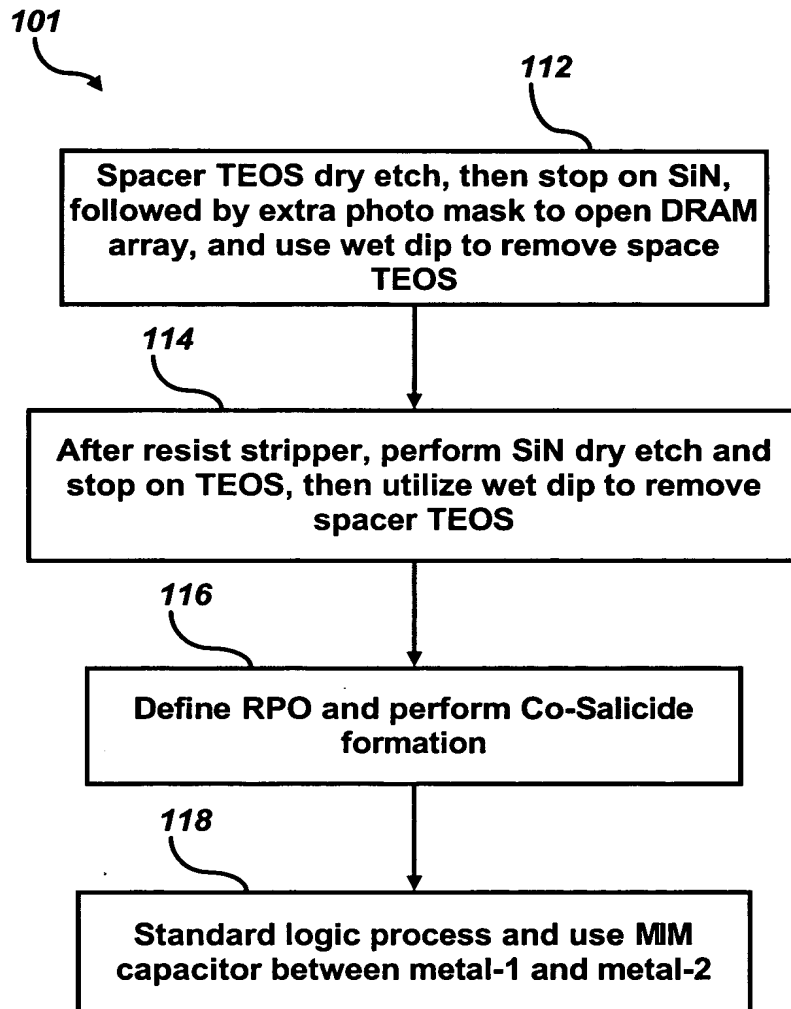


FIG. 17